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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO	
10/649,425	08/27/2003	James J. Chambers	TI35714	5169	
23494	7590 05/16/2005		EXAM	EXAMINER	
TEXAS INST	TRUMENTS INCORPOR	SARKAR, ASOK K			
P O BOX 6554	474, M/S 3999				
DALLAS, TX 75265			ART UNIT	PAPER NUMBER	
			2891		
			DATE MAILED: 05/15/2004		

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)				
	10/649,425	CHAMBERS, JAMES J.				
Office Action Summary	Examiner	Art Unit				
	Asok K. Sarkar	2891				
- The MAILING DATE of this communication appears on the cover sheet with the correspondence address - Period for Reply						
A SHORTENED STATUTORY PERIOD FOR REPLY THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a reply - If NO period for reply is specified above, the maximum statutory period v - Failure to reply within the set or extended period for reply will, by statute, Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	36(a). In no event, however, may a reply be timed within the statutory minimum of thirty (30) days will apply and will expire SIX (6) MONTHS from a cause the application to become ABANDONE	nely filed s will be considered timely. the mailing date of this communication. D (35 U.S.C. § 133).				
Status		·				
1) Responsive to communication(s) filed on 20 A	oril 2005.					
2a)⊠ This action is FINAL . 2b)☐ This	This action is FINAL . 2b) This action is non-final.					
Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.						
Disposition of Claims						
4) ☐ Claim(s) 1,6-14 and 21-26 is/are pending in the 4a) Of the above claim(s) is/are withdraw 5) ☐ Claim(s) 21-26 is/are allowed. 6) ☐ Claim(s) 1 and 6-14 is/are rejected. 7) ☐ Claim(s) is/are objected to. 8) ☐ Claim(s) are subject to restriction and/or	wn from consideration.					
Application Papers						
9)☐ The specification is objected to by the Examiner.						
10) The drawing(s) filed on is/are: a) □ accepted or b) □ objected to by the Examiner.						
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).						
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d). 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.						
Priority under 35 U.S.C. § 119						
 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some color None of: 1. Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No. 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received. 						
. Attachment(s)						
1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date	4) Interview Summary Paper No(s)/Mail Da 5) Notice of Informal P 6) Other:					

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DETAILED ACTION

Response to Arguments

1. Applicant's arguments with respect to claims 1 and 6 – 14 have been considered but are most in view of the new ground(s) of rejection.

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.
- 3. Claims 1, 6, 7, 9 12 and 14 are rejected under 35 U.S.C. 102(e) as being anticipated by Takeuchi, US 2004/008001.

Regarding claim 1, Takeuchi teaches a method of forming dual work function metal gate electrodes in a semiconductor device, comprising:

- forming a gate dielectric 121over a substrate 101 (see Fig. 5A);
- depositing a mold layer 122 having a first opening 145A therein over said gate dielectric 121 (see Fig. 5B);
- creating a first metal gate electrode by depositing a first metal 111 in said first opening (see Fig. 5C);
- then, etching said mold layer to form a second opening 145 B, and

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depositing a second metal 112 in said second opening 145B to form a second metal gate electrode (see Fig. 5C) in paragraphs 108 – 118.

Regarding claim 6, Takeuchi teaches the mold layer is an inorganic material in paragraph 110.

Regarding claim 7, Takeuchi teaches removing the mold layer after depositing the first and second metal with reference to Fig. 6C.

Regarding claim 9, Takeuchi teaches first metal zirconium in paragraph 63.

Regarding claim 10, Takeuchi teaches metals of Pd, Rh, Co and Ni in paragraph 43.

Regarding claim 11, Takeuchi teaches removing excess first and second metals with reference to Figs. 5D and 6B.

Regarding claim 12, Takeuchi teaches removing by CMP in paragraphs 113 and 116.

Regarding claim 14, Takeuchi teaches forming source and drain structures with reference to Fig. 6D.

Claim Rejections - 35 USC § 103

- 4. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

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The factual inquiries set forth in *Graham* v. *John Deere Co.*, 383 U.S. 1, 148 USPQ 459 (1966), that are applied for establishing a background for determining obviousness under 35 U.S.C. 103(a) are summarized as follows:

- 1. Determining the scope and contents of the prior art.
- 2. Ascertaining the differences between the prior art and the claims at issue.
- 3. Resolving the level of ordinary skill in the pertinent art.
- 4. Considering objective evidence present in the application indicating obviousness or nonobviousness.
- 5. Claim 13 is rejected under 35 U.S.C. 103(a) as being unpatentable over Takeuchi, US 2004/008001.

Takeuchi teaches removing by etching in paragraphs 113 and 116, but fails to teach dry etching.

Therefore, it would have been obvious to one with ordinary skill in the art at the time of the invention to modify Takeuchi and use dry etching since dry etching is a standard practice in semiconductor manufacturing.

6. Claim 8 is rejected under 35 U.S.C. 103(a) as being unpatentable over Takeuchi, US 2004/008001 in view of Liang, US 6,130,123.

Takeuchi fails to teach the work function values for the metal electrodes.

Liang teaches first metal has a work function between about 4 and about 4.2 eV and the second metal has a work and between about 5 and about 5.2 eV in column 1, lines 24 – 33 for the benefit of making CMOS device with enhanced short channel effects in column 1, lines 54 – 57.

Therefore, it would have been obvious to one with ordinary skill in the art at the time of the invention to modify Takeuchi and use metal electrodes of appropriate work

function values for the benefit of making CMOS device with enhanced short channel effects as taught by Liang in column 1, lines 54 – 57.

Allowable Subject Matter

- 7. Claims 21 26 are allowed.
- 8. The following is an examiner's statement of reasons for allowance:

Claims 21 – 26 recite, inter alia, a method of forming dual work function metal gate electrodes in a semiconductor device comprising the steps of creating the first electrode by using a first etched mold layer on a substrate and then removing the mold and creating the second electrode using a second mold by etching a second opening and depositing a second metal in that opening. The art of record does not disclose or anticipate the above limitation in combination with other claim elements nor would it be obvious to modify the art of record so as to form a device including the above limitation.

Conclusion

1. 9. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any

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extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

- 10. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."
- 11. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Asok K. Sarkar whose telephone number is 571 272 1970. The examiner can normally be reached on Monday Friday (8 AM- 5 PM).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, William B. Baumeister can be reached on 571 272 1722. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

12. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR.

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Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

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Asol William Sarhar Asok K. Sarkar

May 10, 2005

Primary Examiner